Time: 8 hours

Accomplishments:

* This week I had planned to complete my schematic design. It is nearly complete with some small modifications suggested by Chandler that still need to be implemented.
* I also planned to begin work on my PCB layout, and have completed the RAM and ROM chip wiring as well as some research into optimal PLCC wiring techniques.

Planned work:

* This next week I plan to move my schematic to 100% completion and complete an initial version of my PCB layout. I expect to have enough time during the hours I have set aside already.

Project % complete:

Hardware: 50%

Software : 2%